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1 Efficient interprocessor communication for MIMD 100%



multiprocessor systems

Michel Dubois , Faye A. Briggs

Conference proceedings of the eighth annual symposium on
Computer Architecture May 1981

Several interprocessor communication mechanisms for multiprocessor systems have been proposed. An efficient communication scheme must facilitate high throughput and good response time. We introduce such an efficient scheme, describe the hardware involved, and evaluate its performance. The method is based on a compile-time tagging of shared data and on using different paths for "shared"; (S-) and "private"; (P-) data. The S-data accesses a shared cache on a word-by-word ...

2 Performance analysis of the Alpha 21264-based Compaq ES40 system 100%



Zarka Cvetanovic , R. E. Kessler

ACM SIGARCH Computer Architecture News , Proceedings of the
27th annual international symposium on Computer architecture May
2000

Volume 28 Issue 2

This paper evaluates performance characteristics of the Compaq ES40 shared memory multiprocessor. The ES40 system contains